

D W 2012 B 5 C 29 D

o 1

## 600V 1700V IGBT

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H I % & ' ( : J K L M ! G N E O P Q R S T U 600V V 1700V Planar  
NPT IGBT: W X Y Z [ \ ] ^ \_ ` a b c d E e f 600V Trench PT IGBT: g  
h Z [ \ ] ^ \_ ` a b c d E i j W k A l m n o p q r s t u v A w x y [  
z { | } ~ • ! G " B U # \$ % F 1200V Planar V Trench NPT i j W k A  
! G Q U & < ' ( ) \* + , - . 1 V / X i j F 0 1 600V/1200V/1700V  
NPT IGBT ! i 2 3 F 4 i 5

! G Q \* + 60 6 m e G F IGBT 7 8 ! i 2 3 9 : R S \$ % F 600V  
Planar NPT IGBT i j % ; A < / X = 7 J > 8 ? @ A 100 6 m B i j % ;  
C u D E F U P S G H ( I J K # L ) M N O 2 P Q R M Q S ) # M i T Q S U V  
V # W X Y Z [ ! G F 1700V Planar NPT IGBT i j W k \ ] ^ \_ ` a  
IGBT b c F ` d e # f g A % ; C u D E F Q S R M h 3 Q i R V j 2 # 8  
Y Z [

! G IGBT i j W k F P k l m F n m o 2 = p F q v A r s ; < c  
M t u v N w J x 2 L y z { N O 2 S # V h 2 S # e f j 2 # 8 F S | A }  
~ • < ' # W X M Q S R M U P S Y 2 F Z [ q v ! G B c IGBT  
i j W k F P k Q I U 1200V Planar V Trench NPT IGBT  
Q F 2011 B \$ % 600V Trench PT e f 600V V 1700V Planar NPT IGBT

W k Q F w R S T U 2500V Planar NPT IGBT F n o V V n o Q  
q r s t A w [ z ~ • } ~ A 600V V 1700V Trench NPT IGBT M  
3300V/4500V Planar FS IGBT n o DC {  $V_{(BR)CES}$  V  $V_{CE(on)}$  Q q r  
W

! G & < ' ( ) e b > 4 i l m ; < c Z [ F % T A  
2005 B DMOS i j \$ % e A U # R S \_ \$ % 6 ` a W X \_ 400-700V  
MOSFET M6 ; a W X \_ 50-200V MOSFET M8 a g h \_ 20-40V  
MOSFET M8 ; a g h \_ 50-80V MOSFET Y F # R i j W k  
B c IGBT l i j W k F R S U A ! G c # R 4 i F i  
j W k \ & 0 X A < ' # R % T F S | A ` o 2 = p  
F < % W V

! " # \$ % & ' ( : e , K L M ! " # \$ N E ! ) \* + , " # \$ T M  
S | V d F ` H I T A ; < V 3 2 F } " # \$ T A  
7 B < ) i T V & ; < # \$ T ' ( e " # \$ %  
T & A U & ; < \* 3 F c ' ( ' ( & ; < ' F c  
' ( A < T , R b > 4 i M ) U # s t M ) U # ! " f # l R \$ % 5-8  
b > % % & 5 ' A B % b > 350 ( ) 8 A < ' c % T \* & + 0 F T  
! " # \$ U l F 2000 B A 2002 B , - < ' 2 F " # \$ T b ) \* A U & ! +  
, " # \$ T M S | V d F T A w F 2003 B . V 6 b > % % & / s  
m F 2006 B 0 x ! 1 STATS ChipPAC S | ) U # ! T F 2007 B . V 8  
b > % % & / s m A w F 2009 B 2 U % A < ' 3 4 5 % 6 M I 7 V Z [ /

l F ` I 7 W F b > % % & 2011 B 8 9 : M ; < " N = > A s l k / ? @ ' ( A c A  
< B C s l ) U # s t ' (

! G H I % & ' ( & ! " # \$ % & ' ( : M ! " # \$ N E + , D E R b >  
4 i T F F T ' ( A F ; < b > 4 i G T H F I A F ; < M J K f L M N \$  
% 0 t B P Q R F S ' ( & < Q R T C i V U U i j I 7 F b > 4 i P A .  
; < ' 2 F 6 R b > 4 i T A \$ % V C 0 1 1 ) 8 6 b > % 2 f 3.5  
) 8 8 b > % 2 A { & Q R T \ F i j W k 0 4 i P W X \ Y Z [ \ ] ^

[www.csmc.com.cn](http://www.csmc.com.cn)

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# 5 (+86-510) 8580 7123-2716

b X (+86) 1586 1580 806

6 7 (+86-510) 8580 4647

# \$ c d [wudn@crmicro.com](mailto:wudn@crmicro.com)

e f

# 5 (+86-510) 8811 5064

b X (+86) 1396 1855 032

6 7 (+86-510) 8811 5555 ext.5604

# \$ c d [jiangy@csmc.crmicro.com](mailto:jiangy@csmc.crmicro.com)